

**In the Claims**

Please cancel claims 1-7 and 15-18, without prejudice or disclaimer.

This listing of claims will replace all prior versions of the claims in this application.

**Listing of Claims**

~~Claims 1-18 (Cancelled)~~

19  
5/2/11  
Claim 19. (New) A coded image capture and decoding system comprising:

a capture system comprising:

an optical system that captures image data from coded targets, so as to generate a plurality of image data groups each representing information concerning a coded target as a whole;

a first processing circuit, coupled to the optical system, that generates a plurality of undecoded images each based on one of the image data groups received from the optical system, so that said plurality of undecoded images each represents information concerning a coded target as a whole;

an image buffer, coupled to the first processing circuit, that stores said plurality of undecoded images generated by the first processing circuit; and

a host system, comprising:

a non-dedicated second processing circuit, for coupling to the image buffer, that, after said plurality of undecoded images each representing information concerning a coded target as a whole, are stored in the image buffer, after a request

by the capture system, and with the non-dedicated second processing circuit having received the plurality of undecoded images from the image buffer so as to have the plurality of undecoded images available at a time for processing, attempts decode processing of said plurality of undecoded images.

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D1  
Claim 20. (New)

A coded image capture and decoding system comprising;

a remote capture unit comprising:

an image buffer that stores a plurality of undecoded images each representative of a coded target; and

a host image processing unit operably coupled to the remote capture unit, the host image processing unit comprising:

a processing circuit operable to effect decoding of undecoded images; and

coded processing circuitry, communicatively coupled to the processing circuit, selectively directing the processing circuit to decode the plurality of undecoded images each representative of said coded target.